

ABSTRACT

Code generator with a plurality of storage elements ( $FF_{1,2,\dots,n}$ ) connected in a code-producing series (R), e.g., flip-flops, wherein the output of the last storage element ( $FF_5$ ) in the series (R) is linked with the input of the first storage element ( $FF_1$ ) in the series (R) to form a circuit, and outputs and inputs of the storage elements are recursively interconnected with EXOR gates inserted. At least one EXOR gate ( $EXOR_{p1}$ ) is provided, whose first input (1) is connected with the output of a storage element ( $FF_1$ ) located in the code-producing series (R), whose second input (2) is connected with the output of another storage element ( $FF_3$ ) located in the code-producing series (R), and whose output (3) is connected with the input of the storage element ( $FF_2$ ) following the storage element ( $FF_1$ ) connected with the first input (1) of the EXOR gate ( $EXOR_{p1}$ ) in the code-producing series (R). The output of a storage element ( $FF_5$ ) located in the code-producing series (R) is connected with the input of an inverter (INV), and the output of the inverter (INV) is connected with the input of another storage element ( $FF_1$ ) arranged in the series (R). (Fig. 1).